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ABSTRACT OF THE DISCLOSURE

A digital television receiver includes a CS receive circuit (12). This CS receive circuit receives a digital television broadcast signal. The digital television broadcast signal contains video data from which first video data is created as well as EPG data from which second video data is created. First and second video compression circuits (18, 20) respectively compress first and second video data and output first and second compression video signals. The first and second compression video signals are composited together by a video compositing circuit (22). Consequently, on the monitor (24) are displayed EPG and broadcast video picture in a 2-screens form.